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FIG. 1

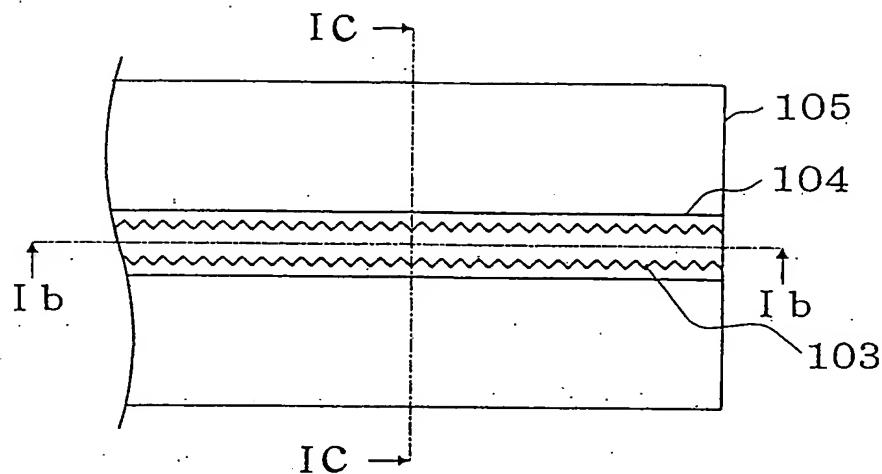


FIG. 2

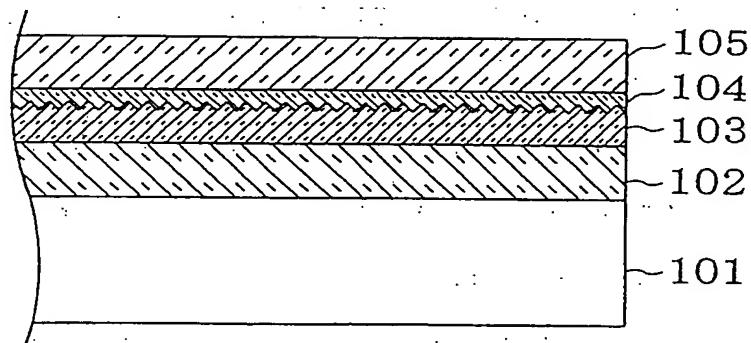
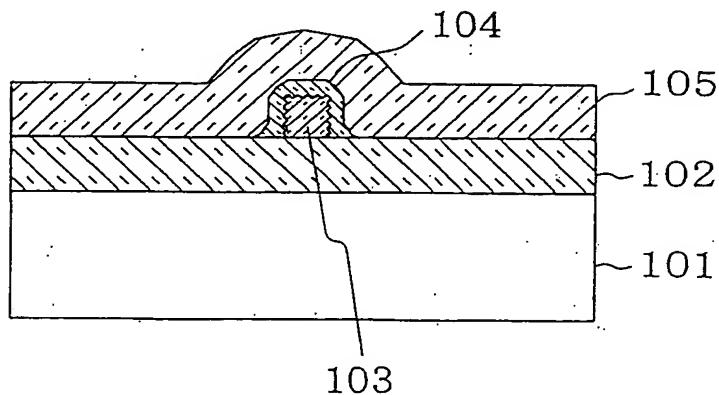


FIG. 3



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FIG. 4

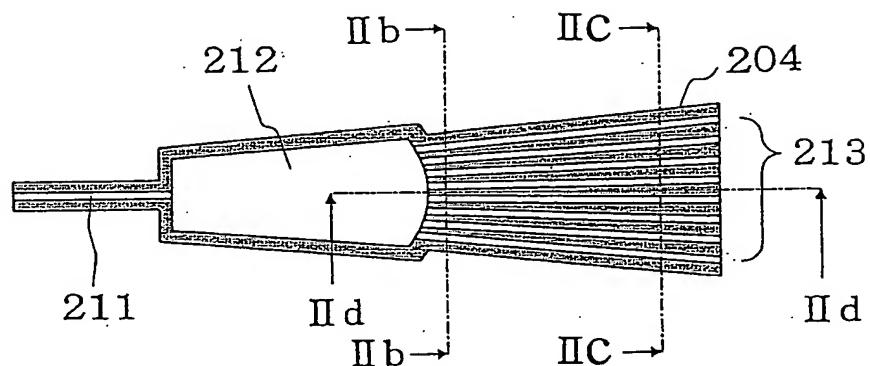


FIG. 5

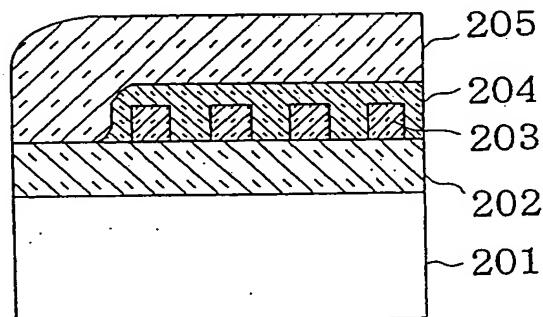
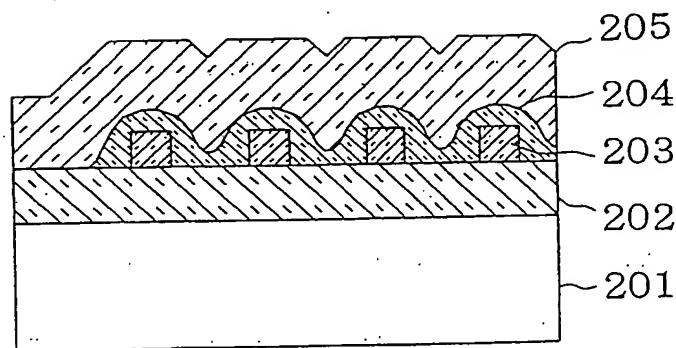


FIG. 6



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FIG. 7

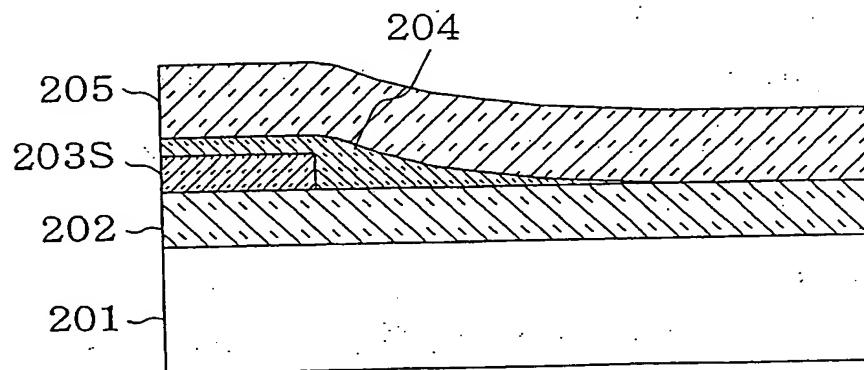


FIG. 8

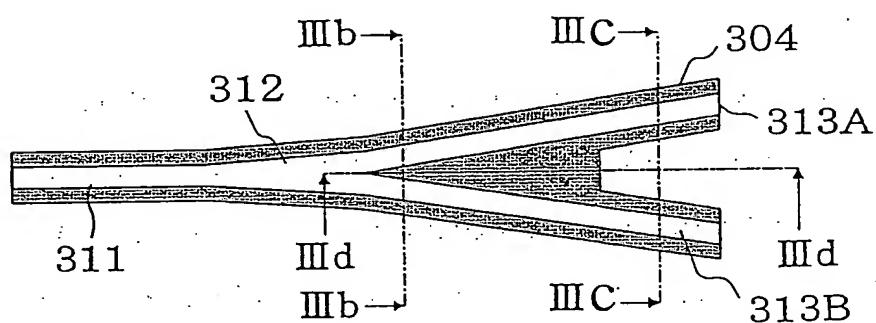
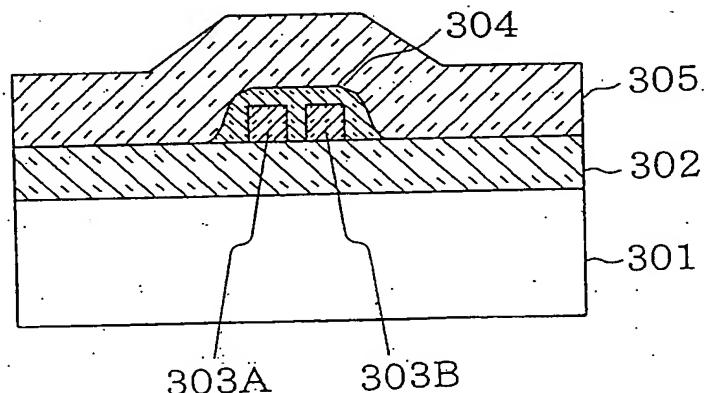


FIG. 9



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FIG. 10

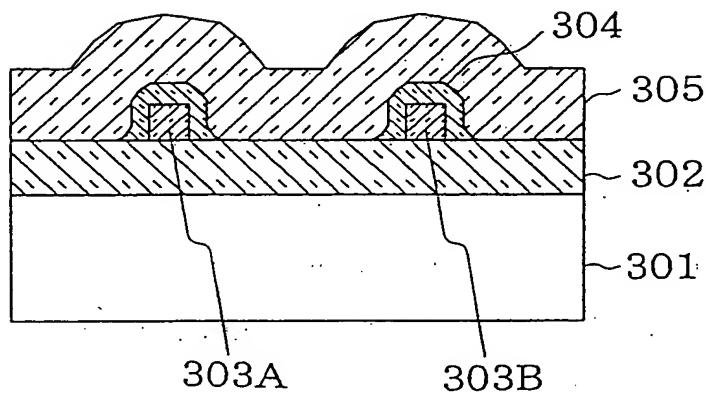
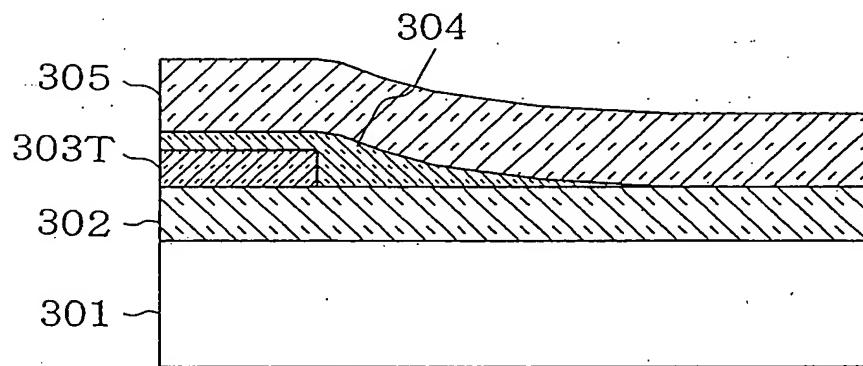


FIG. 11



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FIG. 12

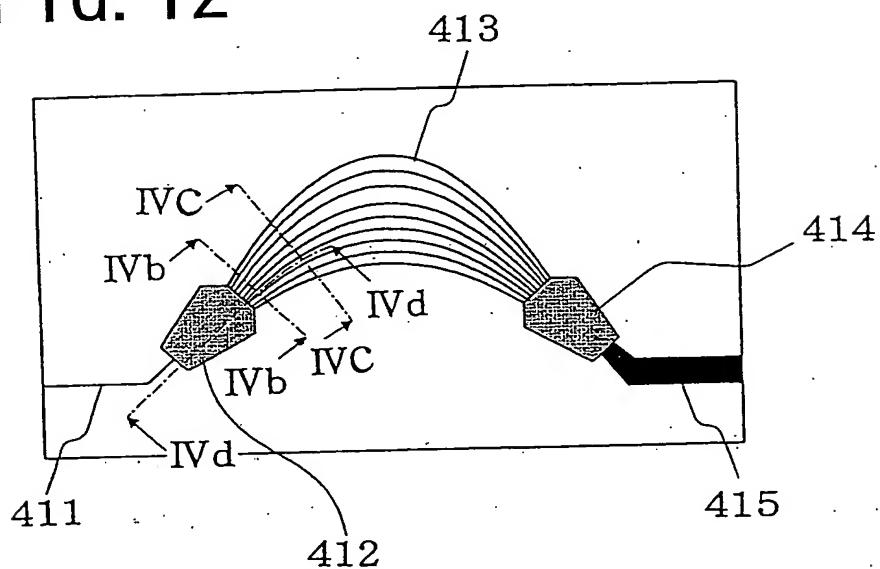


FIG. 13

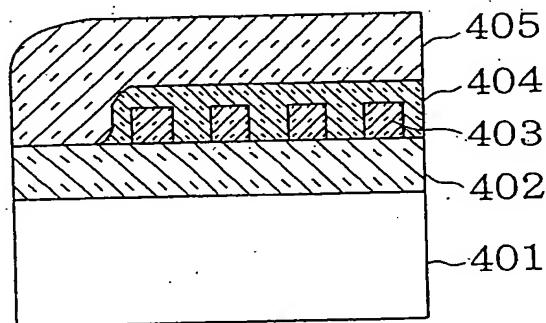
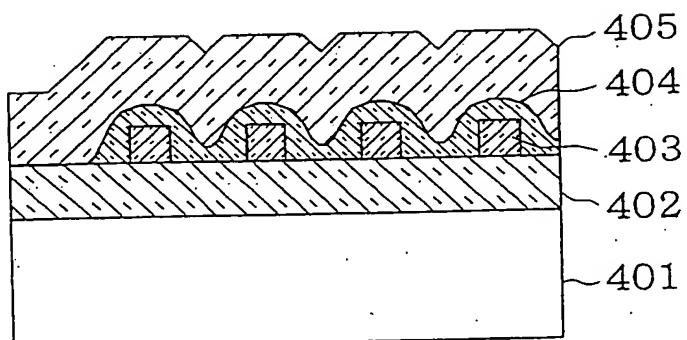


FIG. 14



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FIG. 15

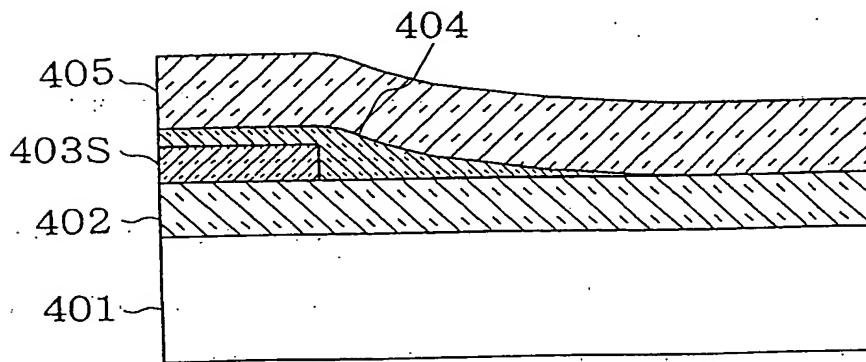


FIG. 16

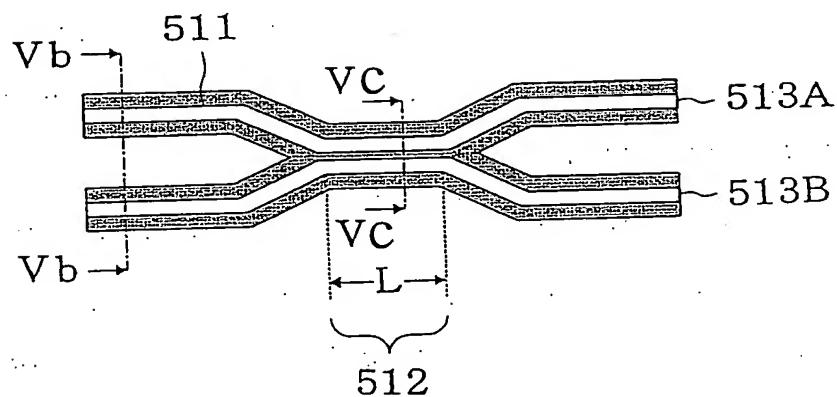
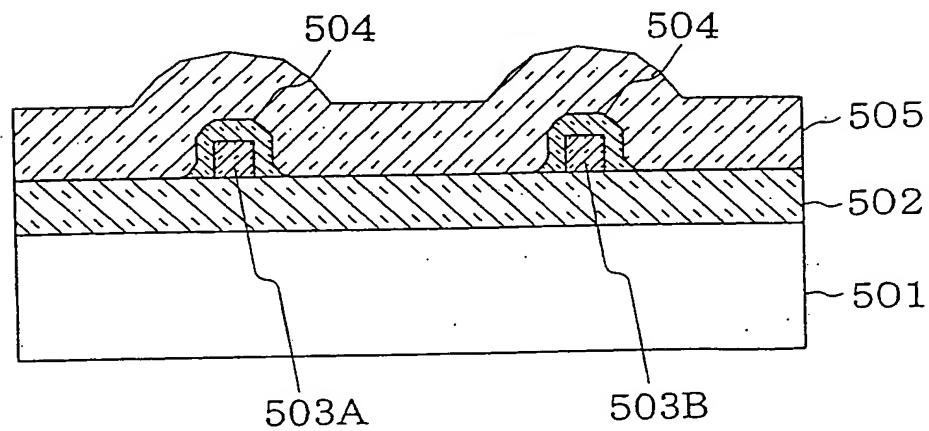


FIG. 17



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FIG. 18

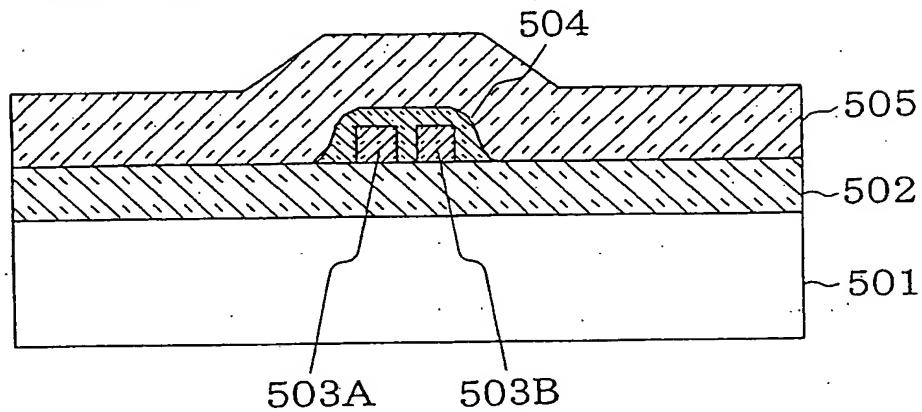


FIG. 19

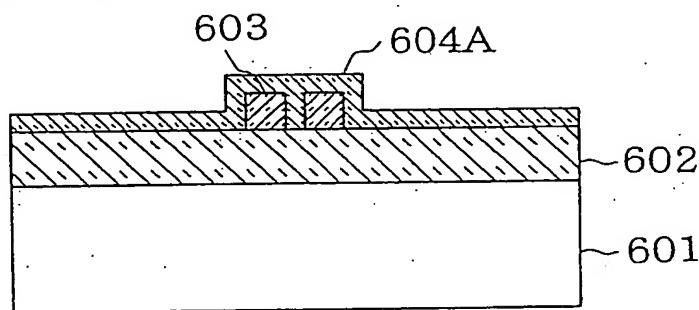
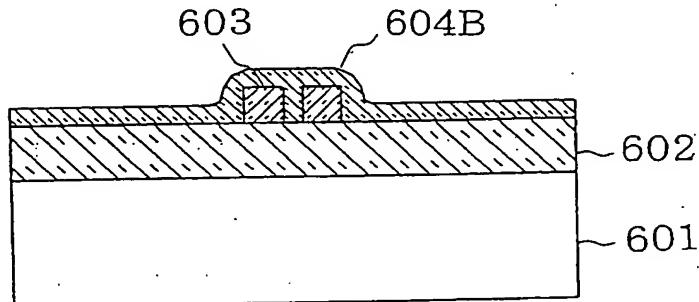


FIG. 20



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FIG. 21

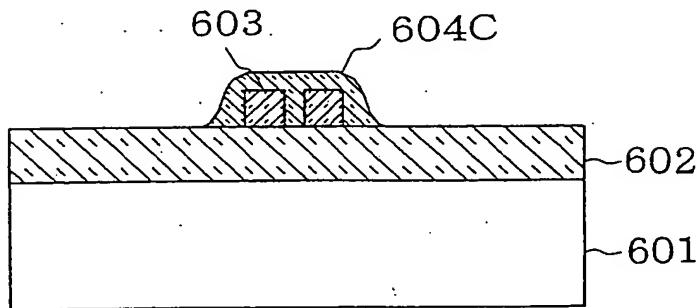


FIG. 22

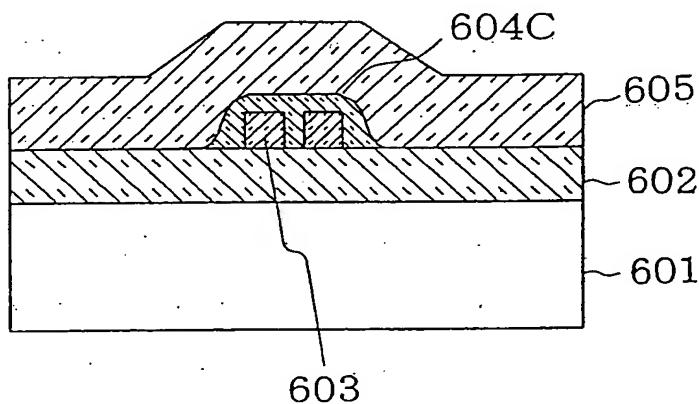
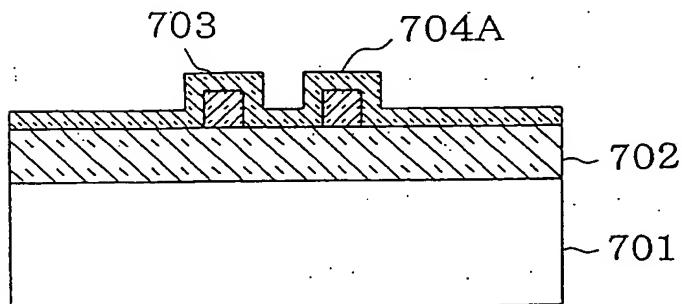


FIG. 23



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FIG. 24

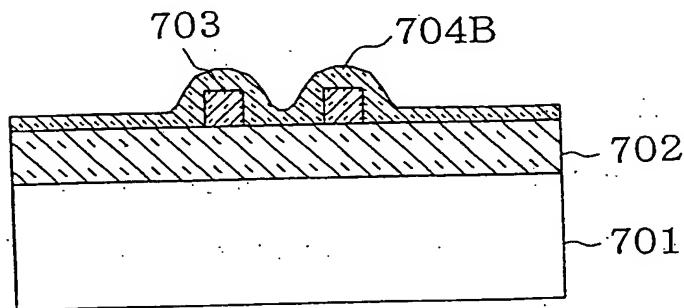


FIG. 25

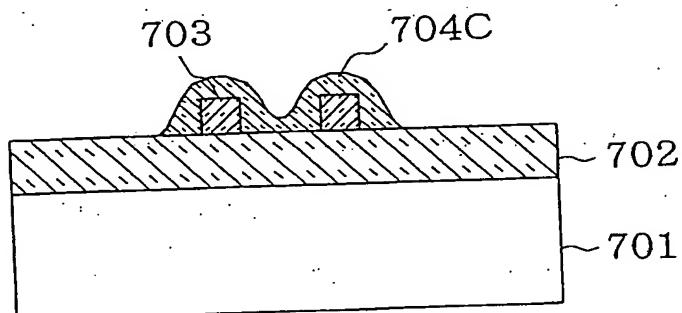
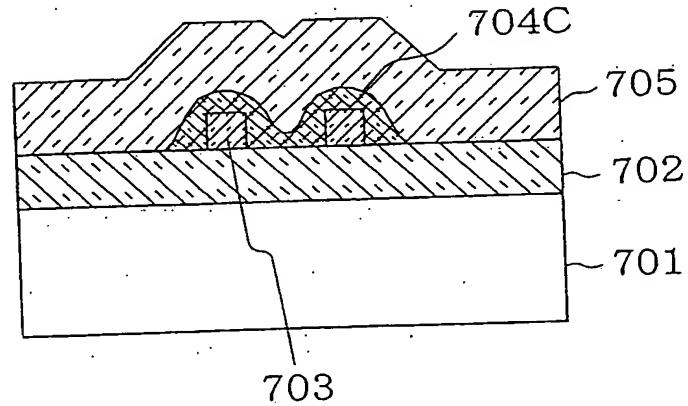


FIG. 26



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DOCKET NO.: 8028-1050
INVENTOR: TSUYOSHI SHIMODA
FILING DATE: DECEMBER 21, 2004
TITLE: OPTICAL WAVEGUIDE CIRCUIT AND MANUFACTURING METHOD THEREOF

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FIG. 27

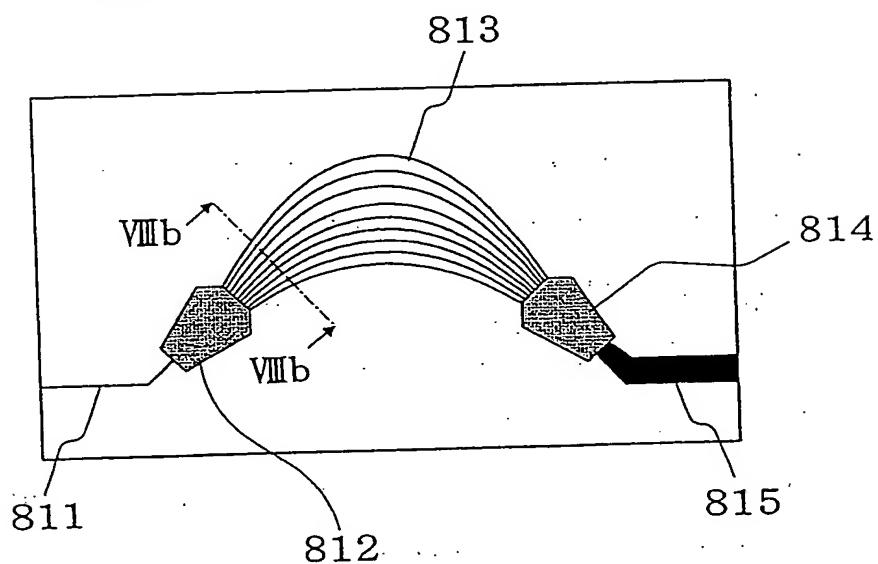
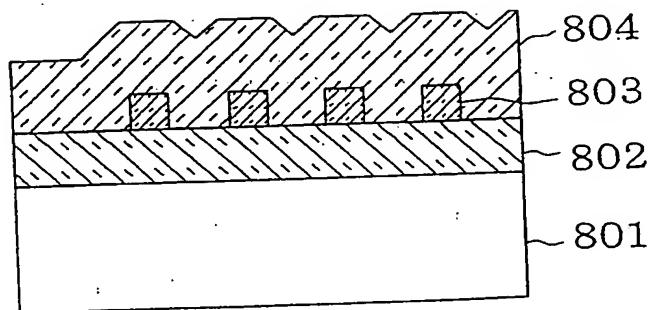


FIG. 28



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FIG. 29

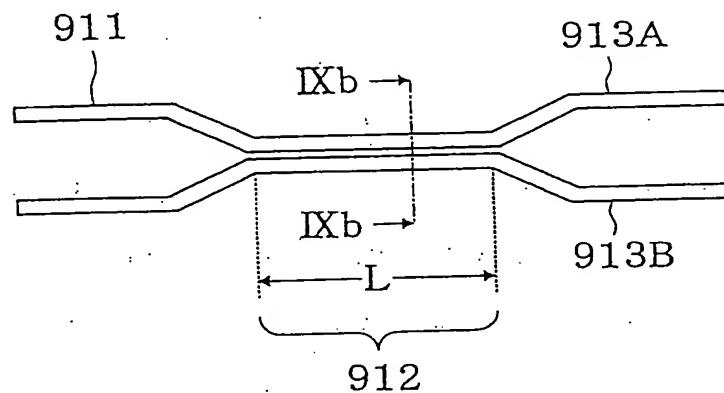
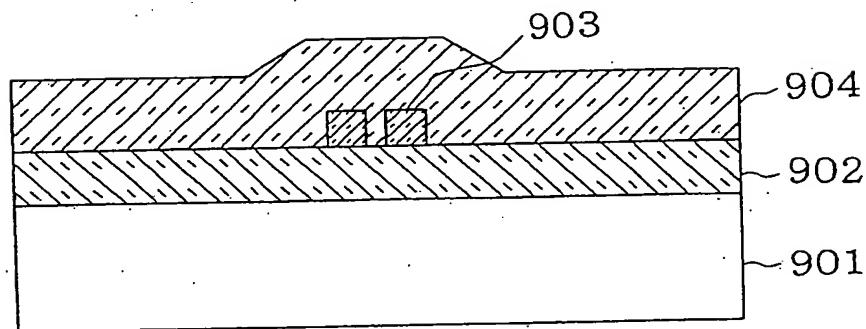


FIG. 30



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FIG. 31

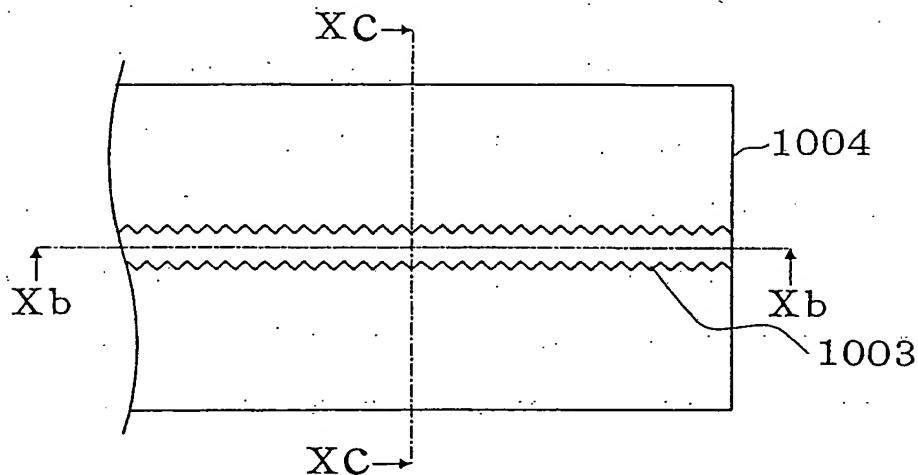


FIG. 32

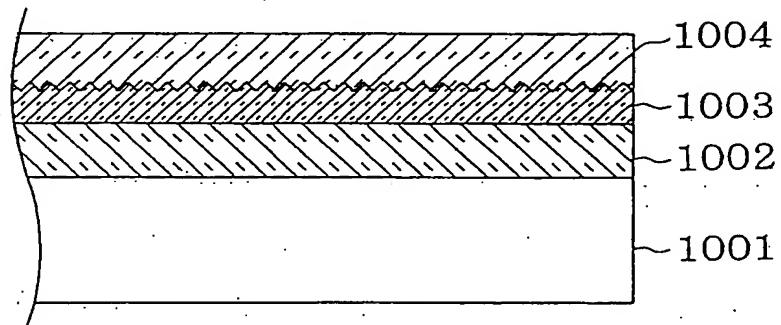
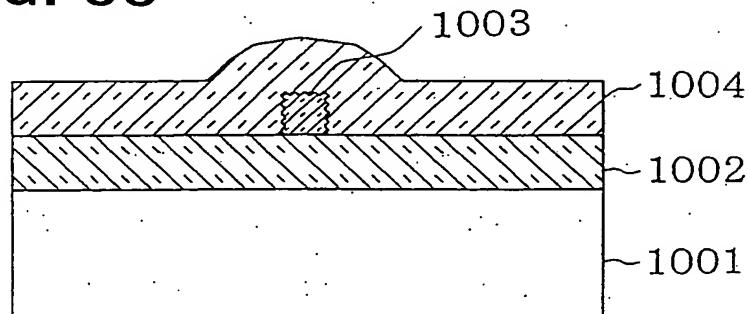
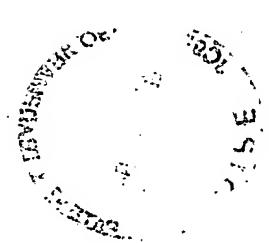
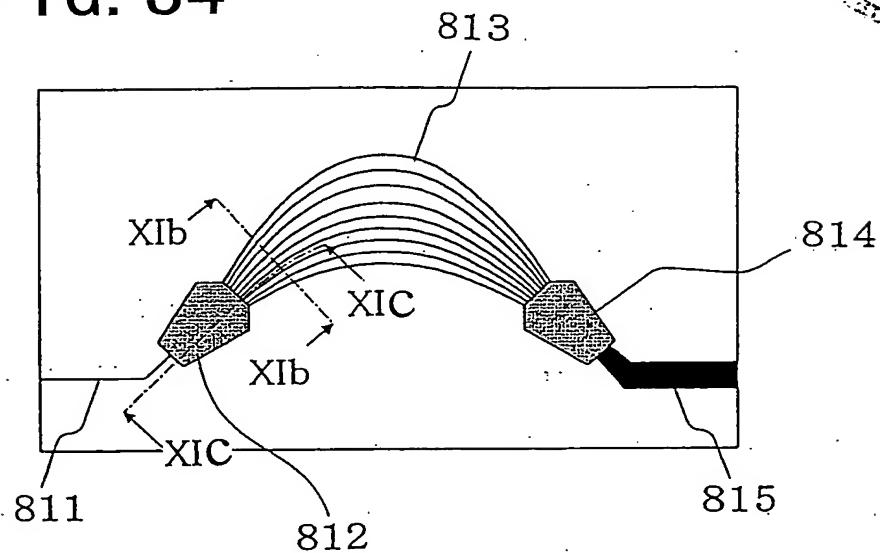
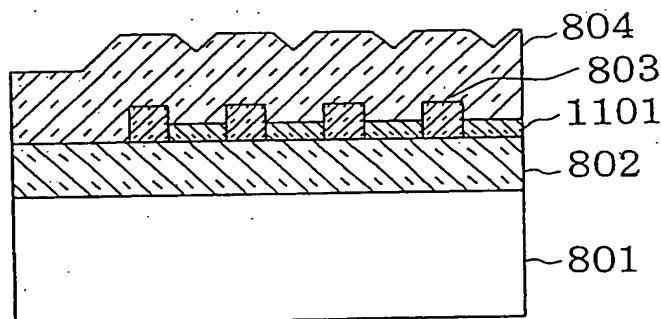


FIG. 33



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**FIG. 34****FIG. 35****FIG. 36**